IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Satoshi Ishikura

Title: SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE DESIGN METHOD, SEMICONDUCTOR DEVICE DESIGN METHOD RECORDING MEDIUM AND SEMICONDUCTOR DEVICE DESIGN SUPPORT SYSTEM

Docket: 31638US4

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98(d), copies of the references are not enclosed because they were previously cited by or submitted to the Patent Office in U.S. Patent Application Serial No.10/278,496, which application is relied upon for an earlier filing date under 35 U.S.C. 120.

If there are any fees resulting from this communication, please charge said fees to our Deposit Account No. 16-0820, Order No. 31638US4.

Respectfully submitted,

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Date: June 20, 2003

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Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date If Appropriate
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OTHER REFERENCES *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	S	
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Examiner:	Date Considered

*Examiner: Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.